



PTO/SB/08A (08-03)

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Sheet 1 of 12

**Complete if Known**

Application Number	10/658,981
Filing Date	September 10, 2003
First Named Inventor	Dale John Shidla
Art Unit	
Examiner Name	unknown
Attorney Docket Number	200310485-1

U. S. PATENT DOCUMENTS

## **FOREIGN PATENT DOCUMENTS**

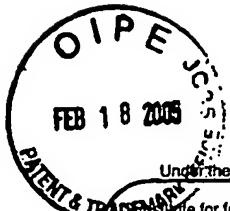
FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>2</sup> -Number <sup>3</sup> -Kind Code <sup>5</sup> (if known)				
BA	1H	GB 1 247 746 A	09-29-1971	IBM		
BA	1I	EP 0 141 744 A2	05-15-1985	Digital Equipme		
BA	1J	JP 60-065348 A	04-15-1985	Panafacom Ltd.		
BA	1K	EP 0 924 603 A2	06-23-1999	Lucent Tech.		

Examiner Signature	/Brian Assessor/	Date Considered	11/01/2006
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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

2

of

2

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Filing Date	September 10, 2003
First Named Inventor	Dale John Shidla
Art Unit	
Examiner Name	unknown
Attorney Docket Number	200310485-1

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
BA	1L	W. HAHN, et al. "Pseudoduplication of Floating-Point Addition-A method of Compiler Generated Checking of Permanent Hardware Faults", 1991, pgs. 161-165, VLSI Test Symposium.	
BA	1M	Patents Act 1977: Search Report under Section 17 for Application No. GB 0 418 638.3	

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